IN THE SPECIFICATION

Paragraph at page 5, line 12:

First, receive a new sampling bit (step 100); store a first sampling bit from a plurality of sampling bits (step 200); compare the voltage level of every sampling bit in the sampling data bits, and determine whether the new sampling bit is a noise bit (step 300). If the plurality of sampling bits numbers three, then step 300 is to determine whether the previous third sampling bit is a noise sampling bit. Also if the plurality of sampling bits numbers three, at the time of the determination the previous third sampling bit will have been stored as a new second sampling bit; the new sampling bit will have been stored as a new third sampling bit; and the previous second sampling bit will have been stored as a new first sampling bit. Pluralities other than three, i.e. pluralities numbering n, where n is an integer, are within the scope of the invention.